

Fig.1.

		WORD ADDRESS		BYTE ADDRESS	INSTRUCTION NUMBER
101	X	0	0	0 0	0
	X	0	0	0 1	1
	X	0	0	1 0	2
	X	0	0	1 1	3
102	X	0	0	1 0 0	4
	13 cj	0	0	1 0 1	5
	X	0	0	1 1 0	6
	0 cj	0	0	1 1 1	7
103	X	0	1	0 0 0	8
	13 cj	0	1	0 0 1	9
	X	0	1	0 1 0	10
	X	0	1	0 1 1	11
104	16 j	0	1	1 0 0	12
	X	0	1	1 0 1	13
	2 cj	0	1	1 1 0	14
	X	0	1	1 1 1	15
105	X	1	0	0 0 0	16
	X	1	0	0 0 1	17
	X	1	0	0 1 0	18
	X	1	0	0 1 1	19

Fig.2.

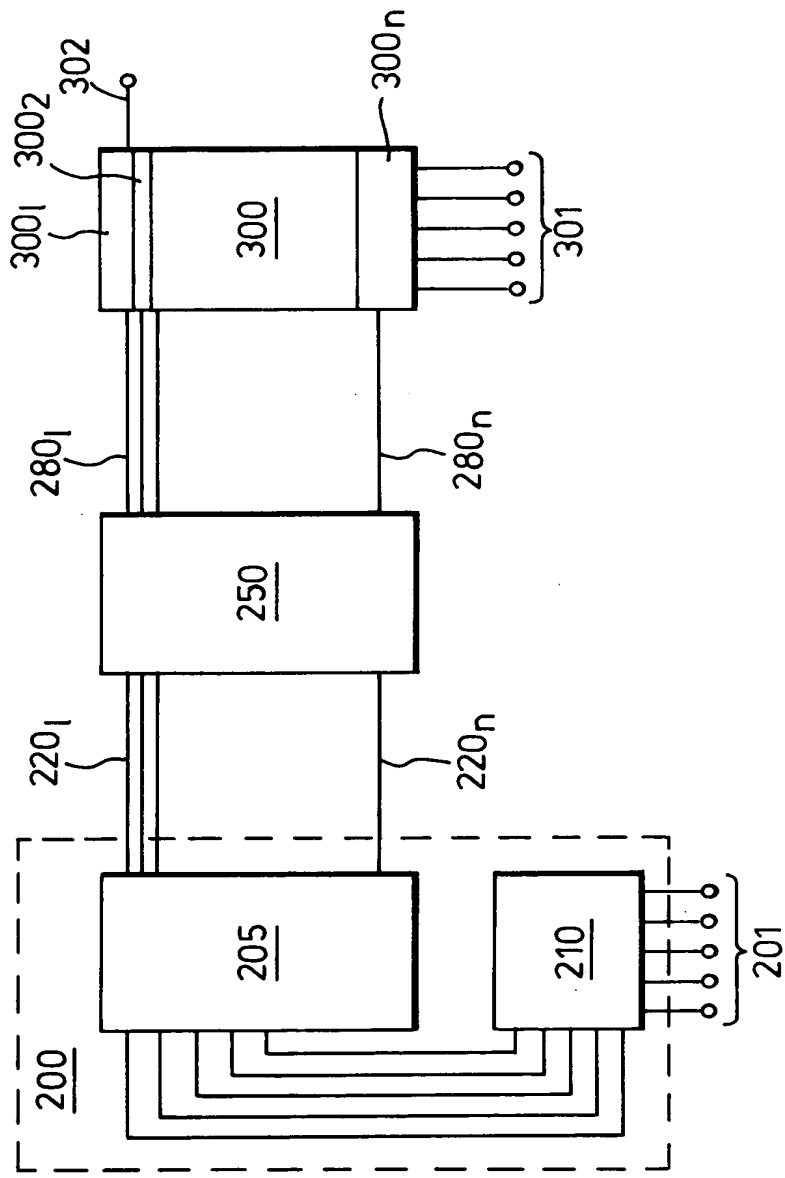


Fig.3.

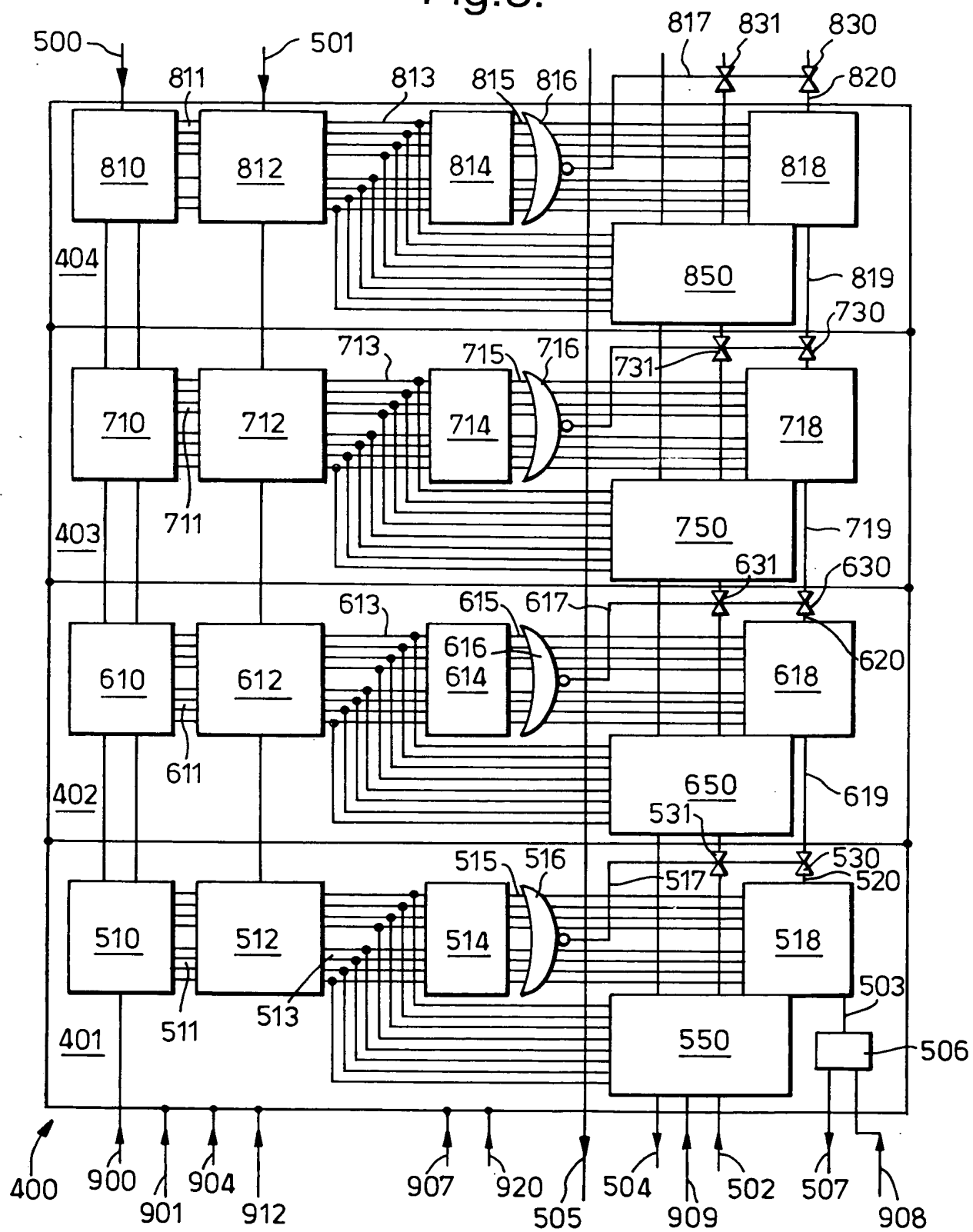


Fig. 4.

The diagram illustrates a system 401 for processing input signals 500 and 501. The system is composed of several interconnected processing units and control elements. At the top, input signals 500 and 501 are received. Signal 500 is processed by a first unit 510, which has input lines 511, 512, 513, 514 and output lines 515, 516, 517. Signal 501 is processed by a second unit 520, which has input lines 521, 522, 523, 524 and output lines 525, 526, 527. The outputs of these units are fed into a third unit 530, which has input lines 531, 532, 533, 534 and output lines 535, 536, 537. The outputs of the third unit are fed into a fourth unit 540, which has input lines 541, 542, 543, 544 and output lines 545, 546, 547. The outputs of the fourth unit are fed into a fifth unit 550, which has input lines 551, 552, 553, 554 and output lines 555, 556, 557. The outputs of the fifth unit are fed into a sixth unit 560, which has input lines 561, 562, 563, 564 and output lines 565, 566, 567. The outputs of the sixth unit are fed into a seventh unit 570, which has input lines 571, 572, 573, 574 and output lines 575, 576, 577. The outputs of the seventh unit are fed into a final output unit 580, which has input lines 581, 582, 583, 584 and output lines 585, 586, 587. Additionally, there is a control unit 502 and a data unit 504 that interact with the processing units.

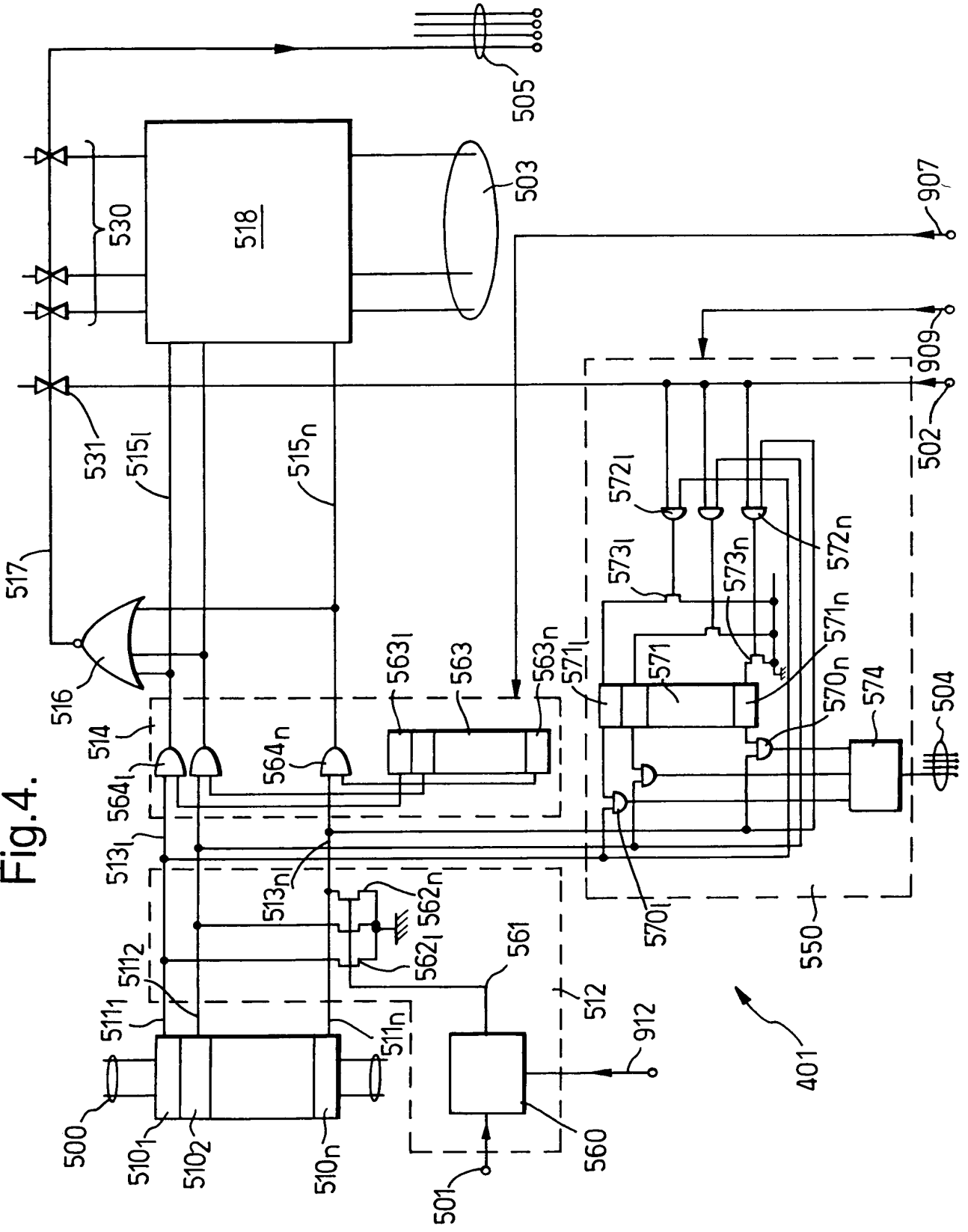


Fig.5.

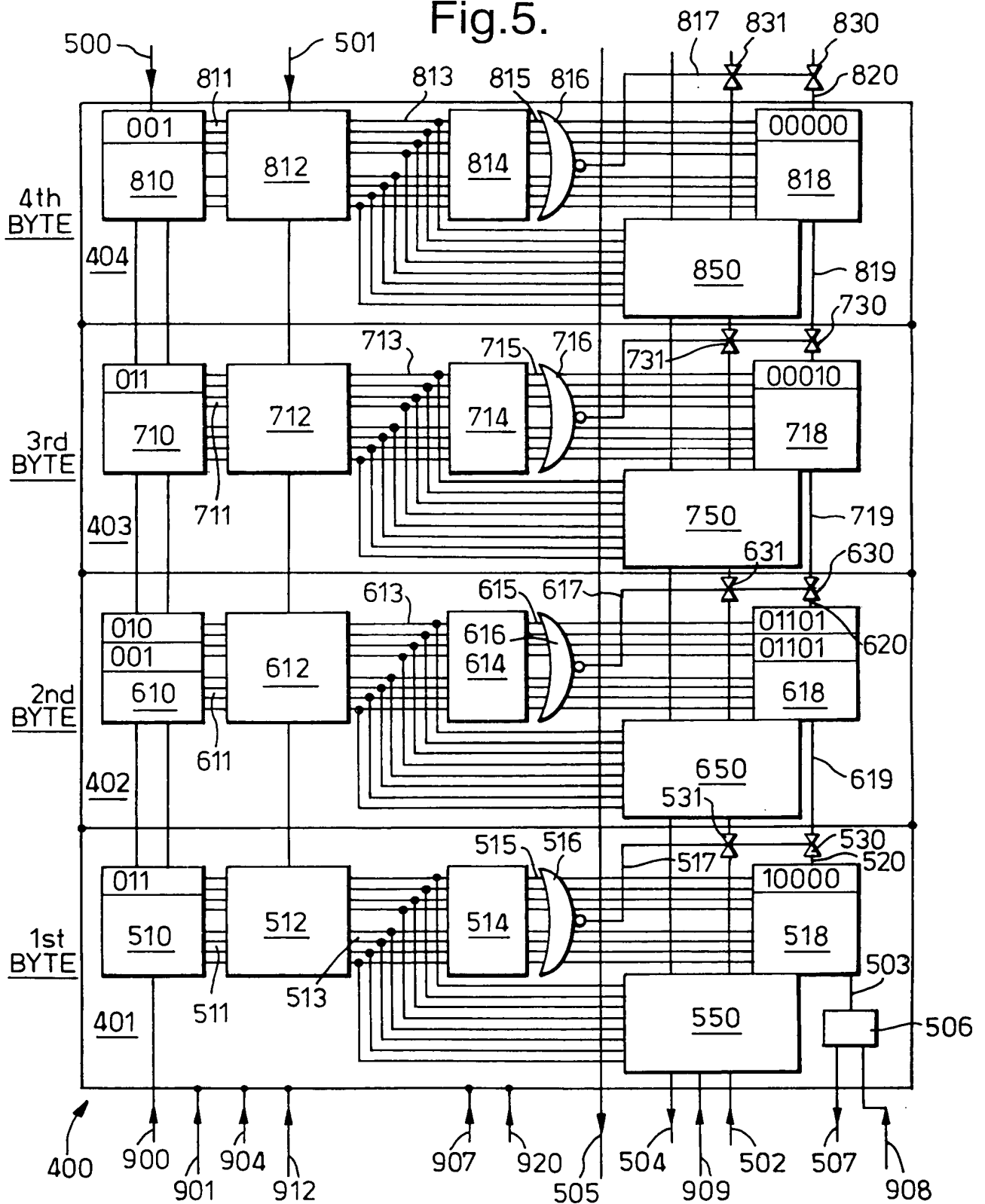


Fig.6.

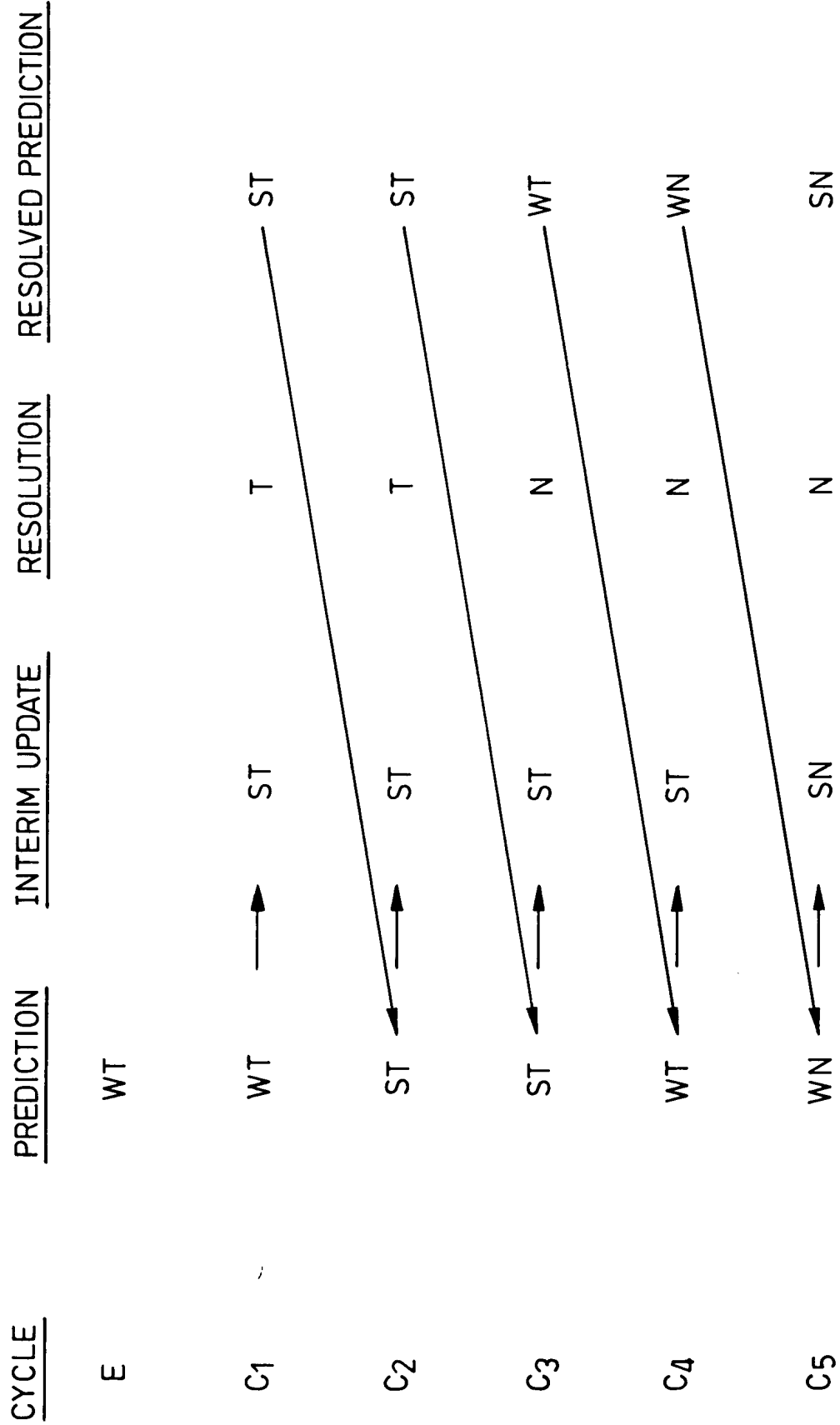
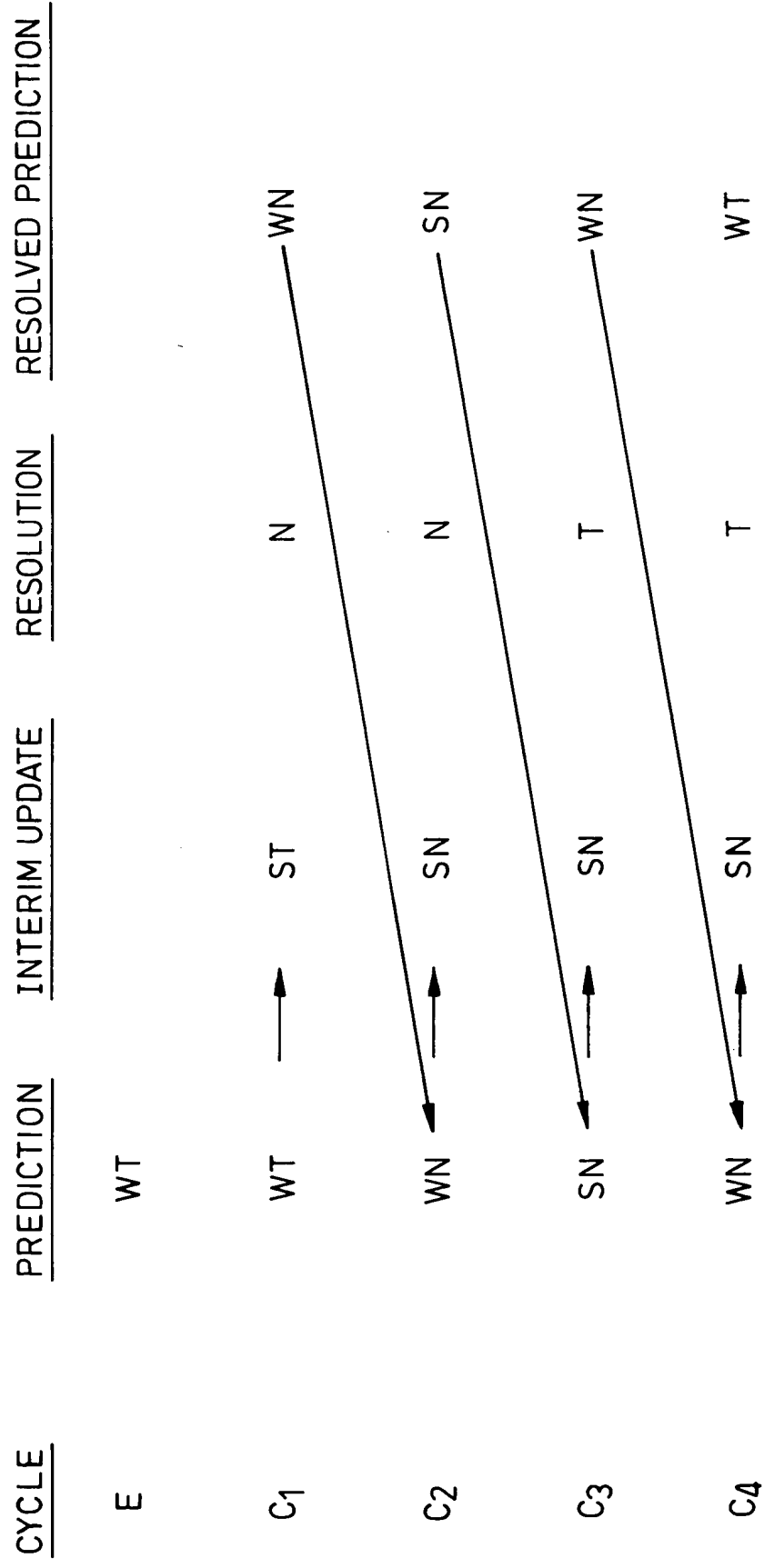


Fig.7.



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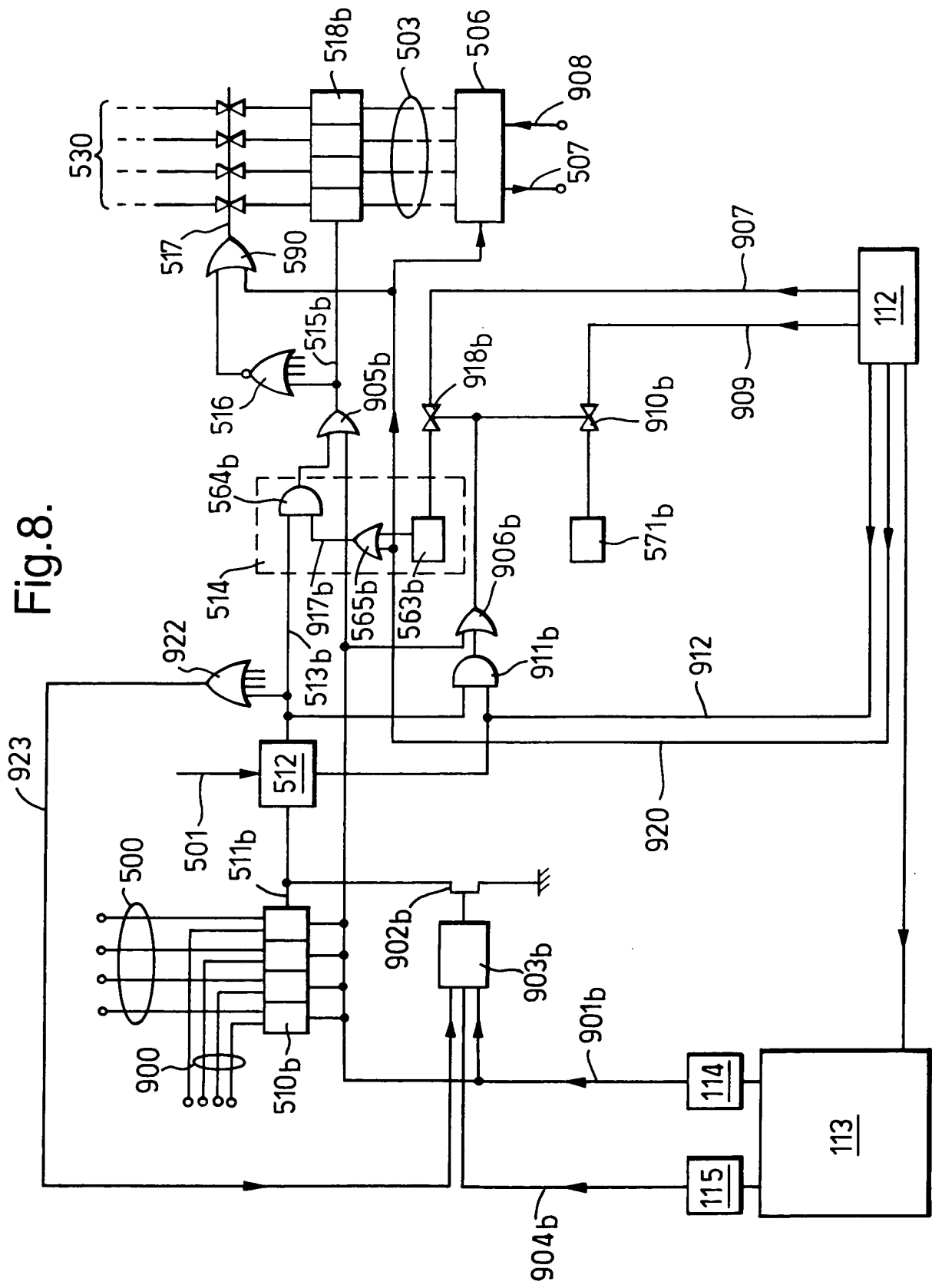


Fig.9.

